

Notice of Allowability	Application No.	Applicant(s)	
	10/710,279	GUENOUN ET AL.	
	Examiner	Art Unit	
	Hirdepal Singh	2611	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 7/23/07.
2. The allowed claim(s) is/are 1-14.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

DETAILED ACTION

1. Acknowledgement is made of the amendment received on July 23, 2007.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Steven Capella on August 29, 2007.

The application has been amended as follows:

In Claims:

- (1) In claim 1, line 3; change "an input reference clock signal," to **-- an input reference clock signal; --**.
- (2) In claim 1, line 5; change "clock phase alignment circuit," to **-- clock phase alignment circuit; --**.
- (3) In claim 1, line 6; insert **-- a --** before "first deviation means".
- (4) In claim 1, line 9; insert **-- a--** before "second deviation means".

- (5) In claim 1, line 12; insert -- a-- before "phase control logic".
- (6) In claim 2, line 3; change "comprises" to -- **comprising**: -- and delete "a clock".
- (7) In claim 2, line 4; insert -- a **clock** – before "management circuit".
- (8) In claim 2, line 5; change "clock signal, and" to -- **clock signal**; and --.
- (9) In claim 2, line 6; change "said phase control logic provides second phase shift signals in response to a comparison of said deviations, each of said second" to -- **a control logic provides second phase shift signals in response to said output clock signal, each of said second** --.
- (10) In claim 3, line 2; change "each include a" to -- **each includes a** --.
- (11) In claim 5, line 9; change "determining a deviation" to -- **determining a first deviation** --.
- (12) In claim 5, line 10; change "where said deviation" to -- **where said first deviation** --.
- (13) In claim 5, line 13; change "checking whether the second" to -- **checking whether a second** --.
- (14) In claim 8, line 2; insert – a – between "further includes" and "phase control logic".
- (15) In claim 8, line 4; insert – **first clock phase alignment** – between "when said" and "circuit has been aligned".
- (16) In claim 9, line 2; insert – **first clock phase alignment** – between "when said" and "circuit has".

(17) In claim 12, line 2; insert – **a** – between “further includes” and “phase control logic”.

(18) In claim 12, line 4; insert – **first clock phase alignment** – between “when said” and “circuit has been aligned”.

(19) In claim 13, line 2; insert – **first clock phase alignment** – between “when said” and “circuit has”.

Allowable Subject Matter

3. Claims 1-14 are allowed.

4. The following is an examiner’s statement of reasons for allowance: The present invention comprises a drift compensation system for adjusting the phase shift between sampling clock and the data clock. The closest prior art, (Bonaccio et al. US 2004/0264619) shows a similar system which also includes two different phase adjusting circuits and a control signal for controlling the oscillator based on the phase adjustment signal. However, Bonaccio fails to disclose the first phase adjustment/alignment circuit is tuned up/down based on the control signal, and when the first circuit is tuned (tune done), the second phase adjustment/alignment circuit starts operating, also for every phase adjustment/alignment operation the number of steps necessary for adjustment are calculated and compared to generate the tune

up/down control signal as claimed. The distinct features have been added to the independent claims 1, and 5. Therefore, rendering them allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hirdepal Singh whose telephone number is 571-270-1688. The examiner can normally be reached on Mon-Fri (Alternate Friday Off)8:00AM-5:00PMEST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shuwang Liu can be reached on 571-272-3036. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

HS
August 29, 2007



SHUWANG LIU
SUPERVISORY PATENT EXAMINER